## **IN THE SPECIFICATION**

Please amend the specification as follows:

## Paragraph 0026:

FIG. 1 is a block diagram showing an exemplary general setup of the digital-audio-signal recording apparatus of the present invention, in which a controller 2 includes an ATAPI (acronym for At [[bus]] Attachment Packet Interface) interface 20, a subcode detection section 21, a digital audio interface 22, and an analog audio interface 23. To the controller 2 (ATAPI interface 20) are connected, via an ATAPI bus 11, a CD drive 4 and a hard disk drive (hereinafter simply referred to as a "hard disk") 5. Further, a FIFO memory 3 and a CPU 1 are connected to the controller 2 (ATAPI interface 20) via a memory bus 12 and a CPU bus 10, respectively.

ROM 6 and a RAM 7, in addition to the controller 2, are connected to the CPU bus 10, and a user interface 8 is connected to the CPU 1.

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